



*JPW*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Angshuman Bezbaruah; Anand A. Kekre; Niranjan S. Pendharkar  
Assignee: VERITAS Operating Corporation  
Title: Fast Failover With Multiple Secondary Nodes  
Serial No.: 10/728,027 Filing Date: December 4, 2003  
Examiner: Unknown Group Art Unit: 2661  
Docket No.: VRT0090P1US

Austin, Texas  
June 8, 2005

COMMISSIONER FOR PATENTS  
P. O. BOX 1450  
ALEXANDRIA, VA 22313-1450

**REQUEST FOR STATUS OF APPLICATION**

Sir:

Applicants hereby request a report on the status of the above-identified patent application. If a telephone call would expedite this request, please call Attorney for Applicants shown below at (512) 439-5086.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: COMMISSIONER FOR PATENTS, P. O. Box 1450, Alexandria, VA 22313-1450, on June 8, 2005.

*D'Ann Naylor Rifai*  
Attorney for Applicants

6/8/05  
Date of Signature

Respectfully submitted,

*D'Ann Naylor Rifai*

D'Ann Naylor Rifai  
Attorney for Applicants  
Reg. No. 47,026  
Telephone: (512) 439-5086  
Facsimile: (512) 439-5099